## Ryerson University Department of Electrical and Computer Engineering COE 608–Computer Organization and Architecture

Midterm Test		March 8, 2016
Name:	Student Number:	
Time limit: 1 hour 40 min		Examiners: N. Mekhiel
Notes:		
a) Closed book.		
b) No calculators.		
c) Answer all questions in the space provided.		Total Marks=30

Q1- Assume the following C code :-

Assume that \$S0 has the address of A[0] and \$S1 has the address of B[0] and i is in \$S3.

1.1-(5 Marks) Write the above code using MIPS instructions.

1.2 (2 Marks) How many instructions are executed during running this code.

1.3 (2 Marks) How many memory data references will be made during the execution.

1.4 (3 Marks) Find the performance of above code in MIPS 1 GHz processor assuming that arithmetic instruction takes 1 cycle, data transfer instruction takes 6 cycles, conditional branch takes 2 cycles and jump takes 1.2 cycle.

1.5 (3 Marks) Find the speed up if system uses a cache to speed the data transfer by 2 times.

Q2.1 (5 Marks) Draw a block diagram for a Multiplication Algorithm

Q2.2 (5 Marks) Apply the above algorithm for the following:

Operation ADD OR SHIFT	Product	Multiplicand	Multiplier
ADD OR SHIFT	0 0 0 0 0 0 0 0 0	0 1 0 0	0 0 1 1

Q2.3- (5 Marks) Determine IEEE754 Floating point representation for **-7.875**